JuPic Programmer

Emulator of PICSTART Plus programmer Works with MPLAB IDE and PICP

http://ajpic.zonk.pl/

Programmer description

The programmer was build with a new processor PIC16F87, which has the self programming function. The device can work with Integrated Development Environment MPLAB[™] IDE (works under operating systems like Windows 98SE, Windows ME, Windows NT 4.0 SP6a WS, Windows 2000 SP2 or Windows XP). This combination makes very useful, efficient and professional tool for each user. Functionality of this pack makes this device easy to use with small, medium or advanced projects and is designed either for beginners or advanced users.

The device is compatible with the original **PICSTART Plus®** programmer and fully integrates with **MPLAB** environment. The programmer is designed to work with FLASH memory **"F"** microcontrollers and with EPROM memory **"C"** family microcontrollers:

> PIC10F2XX, PIC12FXXX, PIC16F6XX, PIC16F7X, PIC16F8X, PIC16F81X, PIC16F7X7, PIC16F8XA, PIC16F8XX, PIC16F8XXA, PIC18FXXXX

PIC12C5XX, PIC12C6XX, 16C4XX, 16C5XX, 16C6X, 16C6XX, 16C7X, 16C7XX, 16C9XX

MPLAB IDE can be fully free downloaded from Microchip web site http://www.microchip.com/. The newest software is available in version "6.xx", "7.xx", the programmer also works with older version "5.70.40".

MPLAB program is designed to work with single source files or whole **projects**. Grouping files with project gives a best way to design and control the application, it increase a comfort of work too.

Program can be use with many useful functions:

- ✓ creating and editing source files
- \checkmark making the projects with files
- \checkmark importing the Intel HEX files
- ✓ debugging the source code
- ✓ assembling, compiling and linking source code
- \checkmark working with time critical signals
- ✓ watching the variables while the program is executing
- \checkmark editing a memory
- $\checkmark\,$ simulating the program
- ✓ sending a code to processor
- ✓ debugging with ICD protocol
- \checkmark exporting and importing data
- ✓ solving the problem with hand help

Programmer installation

- 1. Place the programmer on stable dielectric base
- 2. Plug the serial cable **RS232** to PC computer and programmer
- 3. Plug the power supply unit and next power cable **12V** to programmer
- 4. Place the processor in **DIP holder** or plug the external programming cable into **ICSP socket** (chapter **ICSP connector**)
- 5. Lunch MPLAB IDE program on PC computer (the program is free and can be downloaded from web site: http://www.microchip.com/ or http://ajpic.zonk.pl/, documentation of program can be downloaded too from web site). User's interface depends on installed program version, JuPic programmer works either with old version of program 5.70.40 or with a newest version 6.xx or higher 7.xx
- 6. Before starting to work, the chapter "Programmer configuration" should be read

Working with MPLAB 5.70.40 version

The programmer activation should be done with options:

- 1. Set the type of programmer: Options \rightarrow Programmer Options \rightarrow Select Programmer... \rightarrow **PICSTART Plus Dev. Programmer**
- 2. Set the port the programmer will be work with: Options \rightarrow Programmer Options \rightarrow Communication Port Setup... \rightarrow COMx
- 3. Activate the programmer: PICSTART Plus \rightarrow Enable Programmer

Points 1 and 2 should be done once when the first lunch of MPLAB program occurred because all the settings are saved. This settings can be change later if needed.

When the settings are completed **MPLAB** starts establishing a connection between program and programmer, this window should appear:

Communicating	X
Establishing communications with PICSTART Plus	

Pic. 1 Establishing communication with programmer

In case of any problems of transmission or configuration, MPLAB shows the error window with corresponding statement.

Next appear the firmware version of JuPic programmer.

All descriptions which are displayed as PICSTART Plus programmer but refers to JuPic programmer of course.



Pic. 2 Getting the version of firmware

After establishing connection the user interface should be appear (Pic. 3) and all functions of programmer are ready to use:

PICSTART Plus Device Programmer						
Device Specif	Device Specifications					
Device PIC1	6F628	-	Configur	ation Bits		
ID's and Chec	ksum		Program	n Statistics		
Device <u>I</u> D	7F7F7F	7F	Pass	000000		
Checksum	9D3D		Fail	000000		
Voltages			Total	000000		
VDD Min	5.00	-	F	R <u>e</u> set		
VDD Nom	5.00	-				
VDD Max	5.00	-				
VPP	13.00	-				
🔄 🗌 Low Vol	tage Progr	888				
<u>S</u> QTP File	No SQTI	P File	Being U	sed		
<u>B</u> lank	<u>R</u> ead	Pr	ogram	<u>V</u> erify		
<u>E</u> rase Flash	<u>E</u> rase Flash Device			<u>C</u> lose		
			-			

Pic. 3 User interface

Program/Verify	×
Start Address 120 End Address 01a8 Program Me Configuration ID Location EEPROM D Calibration	emory on Bits 1s Pata Memory
Code Protect Setting Off 400:7FF 200:7FF All	Valid Addresses 0000-07FF 0000-03FF 0000-01FF None
Program Verify	Close

Pic. 4 Part memory programming window

The buttons are:

- Device choosing the processor
- Device ID setting the ID bits
- Configuration Bits setting the Configuration bits
- Blank processor blank check
- Read read the processor code
- **Program** write the processor code
- Verify code verification
- Erase Flash Device erasing the processor, JuPic programmer has this function implemented in hardware (button "ERASE") working independently from MPLAB.
- Close closing the interface

Processor can be program with "Program" button (Pic. 3), after this action all available processor memory will be changed: program memory (FLASH or EPROM), data memory (EEPROM), ID memory (ID Locations) and configuration memory (Configuration Bits).

If only a special part of memories must be program the range memory window should be choose Menu \rightarrow PICSTART Plus \rightarrow Program/Verify..., and next set the right parameters (Pic. 4). Program interface can be use to set program memory ranges too.

Very important stage of programming is preparing the configuration bits. That bits can be set in "Configuration Bits" memory window. Each processor has a different range of settings according to specification:



Pic. 5 PIC16F84

- Pic. 6 PIC16F628
- Pic. 7 PIC16F877

MPLAB environment is easy configurable and user can reach to all the functions and needs. This picture below shows the look of some project:



Pic. 8 Full user interface

Working with MPLAB 6.xx and 7.xx version

The programmer activation should be done with options:

- 1. Set the type of programmer: Programmer \rightarrow Select Programmer \rightarrow **PICSTART Plus**
- 2. Set the port of programmer: Programmer \rightarrow Settings... \rightarrow Communication \rightarrow COMx
- 3. Activate the programmer: Programmer \rightarrow **Enable Programmer**

That steps shown above (except enable) should be set only once at the first startup because MPLAB can remember all parameters. This settings can be also change later if needed.

When enable function is called **MPLAB** sends a request to **JuPic** and after a short while programmer is ready to use. Initialization is completed when device bar stays active:

월 월 월 월 월 문화 Pass: 0 Fail: 0 Total: 0

Pic. 9 Programmer functions

Calling the functions (from left side):

- Blank Check processor blank check
- **Read** read the processor code
- **Program** write the processor code
- Verify code verification
- Erase Flash Device erasing the processor, JuPic programmer has this function implemented in hardware (button "ERASE") working independently from MPLAB.

The right side of abave bar shows a count of succeed writes (Pass), wrong writes (Fail) and total writes (Total) processor memories.

From this moment the programmer is ready to use and the "Work-space" can be build. Before programming the processor type must be set which is used in project: Configure \rightarrow Select Device... \rightarrow **Device** (Pic. 10)

A green led indicator (PICSTART Plus) shows if the processor can be use with this programmer (a list of processors work with JuPic programmer can be read below in last chapter), a yellow led shows limited capabilities with this part (beta support), and a red led shows that this processor can't be programmed by this tool.

Select Device	X
De <u>v</u> ice:	Device <u>F</u> amily:
PIC16F87	▼ ALL ▼
Microch	nip Tool Support
Programmers	
PICSTART Plus	MPLABICD 2 🥝 PICkit 2
🥥 PRO MATE II 🛛 🥥	PICkit 1
🥝 MPLAB PM3 🛛 🥥	MPLAB REALICE
Language and Design Too	
ASSEMBLER O v3.90	COMPILER 🥝 VDI
Debuggers	
🙆 MPLAB SIM 🥥	MPLABICD 2 🥝 PICkit 2
MPLAB REAL ICE	
MPLAB ICE 2000	MPLAB ICE 4000
PCM16YG0	ONO Module
<u> </u>	<u>Cancel H</u> elp

Pic. 10 Processor choosing window

After configuring this settings MPLAB is ready to program a connected processor.

Processor can be program with "**Program**" button, after this action all available processor memory will be changed: program memory (FLASH or EPROM), data memory (EEPROM), ID memory (ID Locations) and configuration memory (Configuration Bits), depends on auto memory range calculation.

If only a special part of memories must be program the range memory window should be choose: Programmer \rightarrow Settings... \rightarrow Memory Ranges (Pic. 11) and set a required parameters. Program interface can be used to set program memory ranges too. All settings are saved in project and actived at next programming call.

Programmer	? ×
Memory Ranges Communications	
Auto select memory areas and range Read all on auto select Manually enter memory areas and range Program memory start address 0 [ff	
Program memory end address	
Erase All Before Program	
OK Anuluj Zastosuj Po	moc

Pic. 11 Setting the range of memory programming

Before program the processor the configuration bits should be set: Configure \rightarrow Configuration Bits... (Pic. 12 and Pic. 13)

Configuration Bits									
Address	Value	Category	Setting						
2007	3F72	Oscillator	HS 🔻						
		Watchdog Timer	UII						
		Power Up Timer	On						
		Brown Out Detect	On						
		Low Voltage Program	Disabled						
		Flash Program Write	Enabled						
		Background Debug	Disabled						
		Data EE Read Protect	Off						
		Code Protect	Off						

Pic.	12	Settina	the	configuration	bits of	16F876
110.		Cernig		configuration	0110 01	101 07 0

Configura	tion Bits		
Address	Value	Category	Setting
300001	26	Oscillator	HS-PLL Enabled
		Osc. Switch Enable	Disabled
300002	OF	Power Up Timer	Disabled
I		Brown Out Detect	Enabled
I		Brown Out Voltage	2.0V
300003	OF	Watchdog Timer	Enabled
I		Watchdog Postscaler	1:128
300006	05	Low Voltage Program	Enabled
		Background Debug	Enabled
		Stack Overflow Reset	Enabled 🗾

Pic. 13 Setting the configuration bits of 18F458 (part)

Settings of ID memory can be easy changed by calling menu window: Configure \rightarrow ID Memory... (Pic. 14)

User ID Memory	×
User ID:	
abcd1234	
Use Unprotected Checksum	
O <u>K</u> <u>C</u> ancel <u>H</u> elp	

Pic. 14 Processor ID memory

The EEPROM memory can be easy modify by calling menu window: View \rightarrow **EEPROM** (Pic. 15)

EEPROM:2																		미뇌
Address	00	01	02	03	04	05	06	07	08	09	0A	0B	0C	0D	0E	OF	ASCII	
0000	F8	EΒ	FC	82	73	03	5C	A6	74	F9	CE	8F	ЗB	5C	B7	DD	s.\. t;\.	
0010	78	70	5C	90	BC	D1	F7	CA	2E	58	09	D6	7F	42	F1	6A	xp\B.;	j 🔤
0020	20	8 F	BE	29	B2	Α9	42	6C	BE	46	67	42	A6	8F	C7	Β6)Bl .FgB	
0030	DB	82	76	50	51	95	22	DE	30	9C	5F	73	CD	EC	3C	B1	vPQ.". 0s<	
0040	D8	C1	1A	44	D7	DD	BE	AF	D2	72	Α4	4B	50	42	96	8C	Dr.KPB.	. =
0050	83	05	7E	87	C0	0B	79	B1	32	22	2C	EA	CE	BA	5B	BA	~y. 2",[
0060	89	47	Β7	DA	C9	E7	F8	F6	1A	45	2C	Β1	23	BD	4 F	ΕA	.G	
0070	D8	C1	1B	ЗD	F1	7B	21	CD	9A	В3	19	41	6C	F4	77	0D	=.{!Al.w	
0080	9C	EA	ЗF	F3	72	0F	19	С9	FD	87	A8	7C	06	47	18	55	?.r .G.U	J

Pic. 15 Processor EEPROM memory

MPLAB is design to work with very advanced projects and it is the easiest way to build an application. The files in project's structure is very readable for user. A tree of project is shown below (Pic. 16):



Pic. 16 Building the project

MPLAB supports very useful and helpful tools either for debugging and simulating program code Debugger \rightarrow Select Tool \rightarrow MPLAB SIM or watching the registers and stack pointer of program:

watch		_ 🗆 ×
Add SFR TRISB	Add Symbol _BODEN	_OFF 💌
Address	Symbol Name	Value
001F	ADCON0	01
008D	PIE2	00
0005	PORTA	00
0001	TMRO	16
0086	TRISB	AF
Watch 1 Watch 2	Watch 3 Watch 4	

Pic. 17 Watching processor registers

📑 Hardwar		
TOS	Stack Level	Return Address
⇒	0	Empty
	1	0205
	2	0591
	3	0000
	4	0000
	5	0000
	6	0000
	7	0000
	8	0000

Pic. 18 Watching program stack

MPLAB has build a new editor where syntax is highlighted, and the program code is more readable.

M	PLAB I	DE v6.30									_ 8 ×
Ele	Edit 1	lew Project	Debugger Progr	ammer <u>lools C</u> or	nfigure <u>W</u> indow <u>H</u> elp		11				
	2	- X -	🖷 🍜 📍	💣 🚅 🔛 🖏	🕸 🛗 Þ 💷 Þ	▶ (+) (+) 🖪	⊧ ¤ <u> </u>	Pass	: O Fail: O Total: O		
	rogra	m Memory						Нак	dware Stack		
		Line	Address	Opcode	Disassembly	7			Steels Low	Detu	
		1	0000	2800	GOTO 0xc			10	5 Stack Leve	o keu	Emotion Sector
		2	0001	3FFF	ADDLW 0xff				•	1	000D
		3	0002	3FFF	ADDLW 0xff				~	2	0000
		4	0003	3FFF	ADDLW 0xff					3	0000
		5	0004	00A0	MOVWF 0x20			EG7E AGM			
		6	0005	0803	MOVF 0x3, 0		INKOTEKI (TOMIC / 13	TO/S.ASPI			
		7	0006	00A1	MOVWF 0x21						
		8	0007	0821	MOVF 0x21, 0		; isr code can	n go here	or be located as	a call sub	routine elsewh
		9	0008	0083	MOVWF 0x3						
		10	0009	0EA0	SWAPF 0x20, 0x1			mowf	status temp w	: retrie	we copy of STA
		11	A000	0E20	SWAPF 0x20, 0			movwf	STATUS	; restor	e pre-isr STAT
	_	12	000B	0009	RETFIE	_		swapf	w temp, f		-
	4	13	0000	23FF	CALL 0x3ff	_		swapf	w_temp,w	; restor	e pre-isr W re
		14	0000	1683	BSF 0x3, 0x5			retfie		; return	from interrup
		15	OUDE	1292	MOVWF UXIU						
		17	00010	1203	ADDIM Owff						
	Special Function Registers					; these first	4 instruc	ctions are not red	quired if t	he internal os	
	Idd		SED Name	Her	Decimal Rina		•	call	0x3FF	; retrie	ve factory cal
ШH	Add	1699 1	JFK Name	nex	Decimal Dina	<u>- y</u>	·	bsf	STATUS, RPO	; set fi	le register ba
		W.	REG	AA	170 10101	010		movwf	OSCCAL	; update	register with
	00	00 II	NDF					bcf	STATUS, RPO	; set fi	le register ba
	00	01 1	MRU CT	00	12 00000	100					
Ш	00	02 5	TATUS	18	24 00011	000					
	00	04 F	SR	00	0 00000	000	; remaining co	ode goes r	here		
	00	05 G	PIO	08	8 00001	000		ORG	0x3ff	: osc ca	1
	00	OA P	CLATH	00	0 00000	000		movlw	0xaa	; save o	ff current W r
	00	0B I	NTCON	00	0 00000	000					-
l F	Out	out				I					
	Build	Denie in Film									
	D Gild										
	Exec	uting: " ed D:\le	C:\Program F rojekt\junic	iles∖MPLAB II \12f675_COD	DENMCHIP_Tools/mpa	asmwin.exe	e" ∕q ∕p12F675	"12£675.	.asm" /1"121675	.lst" /e":	121675.err"
	BUIL	D SUCCEE	DED: Sat Sep	06 11:16:20	2003						
MPLAE	SIM	PICSTAR	T Plus PIC12F675	pc:0xc	V:0xaa z dc c		0x2202				

Pic. 19 Full user interface

Programmer functions

Programmer has many functions expanding its ability:

- ✓ device has build in an erase button, which can be use to erase the processor memories without lunching MPLAB and connecting programmer to computer (even Code Protection), to call this function an "ERASE" button must be pushed and hold down for 2 seconds.
- ✓ an information LED diode "PROG" shows a state of device:
 - two double short blink power on an device initiatializing
 - diode is on programmer is ready to use
 - diode is fast blinking establishing communication with MPLAB
 - diode is slow blinking data transmission is in progress with MPLAB (read/write)
 - diode is off an "ERASE" button is pushed and hold down
 - three short blink the processor was erased
 - diode generates periodically three short blink the programmer is in trap state, there's no active code or the code is damaged and new firmware must be downloaded
- ✓ an external ICSP (In-Circuit Serial Programming) connector was build in, a processor can be programmed in designed board
- \checkmark LVP mode can be activated and use
- ✓ programmer has build in a DIP18 socket, which may be use to fast program a stand alone processor without any additional connecting cables. This socket is designed to work with DIP18, DIP14 and DIP8 compatible processors too (excluding PIC10FXXX).
- ✓ a configuration jumpers can easy suits the programmer to own hardware requirements
- ✓ programmer can be connected directly to serial computer port COM without serial cable
- ✓ ICs are place with additional sockets and can be easy replace with a new ones in case of damage.
- $\checkmark\,$ a write procedures was optimized and work faster now.
- ✓ a feature of upgrading new firmware through serial port was added.

JuPic programmer can be connected with the computer through serial connector RS-232C and straight through cable (**modem**), which is the same for original PICSTART Plus programmer.

The cable for serial connection has one male plug **DB-9M** at programmer side and a female one **DB-9F** or **DB-25F** at computer side. This cable can be hand made; the signal used by programmer are listed below in table.

Cional	DB-25	DB-9	Direction	DB-9	Cional	
Signai	Female		PC — JuPic	Male	Signal	
ТХ	2	3	\rightarrow	3	RX	
RX	3	2	\leftarrow	2	ТХ	
DTR	20	4	\rightarrow	4	Data Ready	
GND	7	5		5	GND	
DSR	6	6	\leftarrow	6	pull up +5V	
RTS	4	7	\rightarrow	7	CTS	
CTS	5	8	\leftarrow	8	RTS	

Table 1 Signals of serial cable for programmer

For serial connection a programmer is communicating with standard serial RS232 protocol speed **19200Kb/s** and **8N1** frame. Data sending is half duplex mode with hardware flow control handshaking by lines **RTS** and **CTS**. New firmware has impemented a new software FIFO which should resolve the problems with some notebooks and Windows XP low level serial port drivers.

Programmer configuration

There are 2 modes for programming the processor:

✓ high voltage HVP (14V) — jumper "LVP ON" open

✓ low voltage LVP (5V) — jumper "LVP ON" short

There are 4 modes for power the device:

1 SAFE mode

This mode is activated when "SAFE" jumper is short



Pic. 20 SAFE mode configuration

This mode can be used for the most safe processor programming. The processor power is activate only while programming is in progress (read/write) and after transmission a power is cut off. It's not recommended to power an external circuit in this mode through **ICSP** connector because the transistor switch can damage this way.



Pic. 21 SAFE mode connection

2 VCC mode — with directly power This mode is activated when "VCC" jumper is short



Pic. 22 VCC mode configuration

This mode can be use to power an external circuit or processor from programmer board. A voltage is present for all time through the ICSP connector. This way an external circuit don't need a additional power supply unit. There is a one very important restriction when use this mode because a current efficiency of programmer stabilizer in not to much (about 100mA) and only a low current circuits can be powered this way.



Pic. 23 VCC mode connection

3 Dependent Mode — without power supply unit This mode is activated when "VCC" and "LVP ON" jumpers are short



Pic. 24 Dependent mode configuration

This mode can be use to work programmer without connecting a power supply unit. The voltage for power the programmer (5V) is taken from designed board through **ICSP** connector. Using this mode is only designed for processors which can be program with low voltage (**LVP**) and also it's not allowed to power a programmer in this mode.



Pic. 25 Dependent mode connection

4 Independent Mode — with two power supplies units This mode is activated when "SAFE" and "VCC" jumpers are open



Pic. 26 Independent mode configuration

This mode can be used when programmer and designed board are independent powered through different power supply units. A programmer is powered with its own stabilizer and the processor is powered from designed board. This mode is most often use with ICSP because this way a designed board has no restrictions for current load and others modules can be easy connected to designed system.



Pic. 27 Independent mode connection

ICSP connector

The programmer has two 6 pin **ICSP** connectors "**SIP6P**" and "**RJ-12**" type, which can be use to programming processor at the designed board without placing it to programming socket (Pic. 28).

There is also installed a precision 18 DIP socket which can be use for directly programming a stand alone processors: PIC12FXXX, PIC12CXXX, 14 pins: PIC16F6XX and 18 pins: PIC16CXXX, PIC16FXXX, PC18FXX20. The way of placing processors in socket is shown below Pic. 28. Detailed ICSP connector description is shown in Table 2. Programming a bigger processors can be done with external DIP adapter or programming through ICSP connector. It is possible to install a **ZIF** socket in place of standard precision socket too.



Pic. 28 ICSP connector description, pin 1.

While working with **ICSP** connector and designed board, the some important rules must be kept:

- * "MCLR" signal should be connected directly to processor, if external power on reset is required (RC delaying circuit) this line must be cruelly separate with signal diode (e.g. 1N4148) or single resistor (1K) (Pic. 29) because in other case the processor can't be enter to programming mode.
- ✓ "DATA" line and "CLOCK" line should be also connected directly to processor. That lines can be use as usual I/O port if designed board won't input a noise signal. The most simple way in this case is connecting a two microswitches to lines RB6 and RB7, which are normally open while the programming is in progress. When programming is completed this lines are cut off from board.

- ✓ "LVP/PGM" line can be use for programming the processor in low voltage mode. Port RB3, 4 or 5 (processor depended) and can't be the same use as usual I/O line.
- ✓ power supply modes are shown in "Programmer configuration" chapter.



Pic. 29 Connecting the programmer through ICSP connector

Pin	Signal	Port
1	VPP	MCLR
2	VCC	VDD
3	GND	VSS
4	DATA	RB7
5	CLOCK	RB6
6	PGM/LVP	RB3/4/5

Table 2 Pinout of ICSP connector (Microchip standard)

The programming protocol use in this device has implemented all time critical and electric parameters needed for regular processors operations and is restricted with **Microchip**[®] technical documentation.

Full description of this programmer in PDF file can be downloaded from web site: <u>http://ajpic.zonk.pl/</u>

Upgrading the programmer

The programmer has built a bootloader module and can be easy upgrade with a new firmware version of code. There are two steps for reprogramming the processor.

Step 1 Preparing and configuring PC hardware and software

- ✓ Install a free serial terminal (e.g. Tera term Pro, can be downloaded from web site: <u>http://ajpic.zonk.pl/download/ttermp23.zip</u>)
- ✓ Launch the terminal and configure parameters of port COMx: Menu → Setup → Serial port... → COMx, 19200, 8n1, hardware.

Tera Term: Serial port setup						
Port:	СОМ1 • ОК					
<u>B</u> aud rate:	19200 💌					
<u>D</u> ata:	8 bit Cancel					
P <u>a</u> rity:	none 💌					
<u>S</u> top:	1 bit 💌 <u>H</u> elp					
Elow control:	hardware 💌					
Transmit delay 0 msec <u>/c</u> har 0 msec/ <u>l</u> ine						

✓ Save the settings: Menu \rightarrow Setup \rightarrow Save setup... \rightarrow 'teraterm.ini'

After configuring all of required parameters, a test of connection should be done:

- 1. Connect the programmer to computer
- 2. Power the programmer
- 3. Launch the serial terminal
- 4. Push "s" (serial) key on computer keyboard

The serial number should be displayed on terminal, if so, the connection is OK and the programmer can be upgraded in next step.

Programmer can be upgraded under **Linux** too, just configure the port and use a serial terminal (e.g. minicom -oL) Step 2 Installation a new software to programmer.

- 1. Power on the programmer
- 2. Lunch the terminal
- 3. Push and hold "ERASE" button on programmer
- 4. Press "u" (upgrade) key on terminal
- 5. Entering into programming mode will be signalized by lighting up the "PROG" diode and displaying ":" character on terminal
- 6. Push "I" (load) key, active code in processor is erased, terminal displays prompt character ">" and programmer waits for a file.
- 7. Send file: Menu \rightarrow File \rightarrow Send file... \rightarrow 'jupic-x.xx.x.hex', where x.xx.x is current version of firmware.

While the new code is loading the terminal displays a progress bar, and the LED "**PROG**" blinks on programmer. After succeeded operation, a new firmware is ready. "**OK!**" string appear on terminal and the programmer automatically starts with a new code. From this moment the upgrade is finished and the programmer is ready to use.

If something goes wrong while upgrading is in progress and some troubles take place (e.g. broke a transmission, power off, loading a wrong code and other bad things) and the programmer can't properly work — don't worry just use **Recovery mode** described below.

Programmer with no active code indicates this state with "**PROG**" diode with three cyclic blinks and terminal shows "**ERROR!**" string, when wrong hex file was sent the programmer can be unpredictable.

Recovery mode:

- 1. Power off the programmer
- 2. Lunch the terminal
- 3. Push and hold "ERASE" button on programmer
- 4. Power on the programmer
- 5. Programmer going to enter into regular upgrade mode (see point 5)

Listing of supported processors

PIC10F200	PIC16F84A	PIC18F2550	PIC16C56
PIC10F202	PIC16F87	PIC18F2580	PIC16C56A
PIC10F204	PIC16F870	PIC18F2585	PIC16C57
PTC10F206	PTC16F871	PTC18F2610	PTC16C57C
PTC10F220	PTC16F872	PTC18F2620	PTC16C58A
PTC10F222	PTC16F873	PTC18F2680	PTC16C58B
	PTC16F8734	PTC18F4220	PTC16C620
PTC12F508	PTC16F874	PTC18F4221	PTC16C6204
PTC12F500	PTC16F8744	PTC18F4320	DTC16C621
PTC12F510	PTC16F876	DTC18F4321	DTC16C6214
DTC12E600	DTC16E8764	DTC18E/321	DTC16C622
DTC12E61E	DTC16F877	PICI8F4331	PICIOCO22 DTC16C622A
PICI2[015		PIC18F4410	PICIOCOZZA DTC14C42A
PICI2F029			PICIOCOZA DICI4C42D
PICI2F030		PICIOF4423	
PICI2F0/0			
PICI2F683		PIC18F4450	PICIOCOSA
DT CI / CEOE	PIC16F886	PIC18F4455	PIC16C642
PIC16F505	PIC16F887	PIC18F4480	P1C16C64A
PIC16F506	PIC16F913	PIC18F4510	PIC16C65A
PIC16F54	PIC16F914	PIC18F4515	PIC16C65B
PIC16F57	PIC16F916	PIC18F4520	PIC16C66
PIC16F610	PIC16F917	PIC18F4523	PIC16C662
PIC16F616	PIC16F946	PIC18F4525	PIC16C67
PIC16F627		PIC18F4550	PIC16C71
PIC16F627A	PIC18F242	PIC18F4580	PIC16C710
PIC16F628	PIC18F248	PIC18F4585	PIC16C711
PIC16F628A	PIC18F252	PIC18F4610	PIC16C712
PIC16F630	PIC18F258	PIC18F4620	PIC16C715
PIC16F636	PIC18F442	PIC18F4680	PIC16C716
PIC16F639	PIC18F448	PIC18F6620	PIC16C717
PIC16F648A	PIC18F452	PIC18F6720	PIC16C72
PIC16F676	PIC18F458	PIC18F8620	PIC16C72A
PIC16F684	PIC18F1220	PIC18F8720	PIC16C73A
PIC16F685	PIC18F1230		PIC16C73B
PIC16F687	PIC18F1320	PIC12C508	PIC16C745
PIC16F688	PIC18F1330	PIC12C508A	PIC16C74A
PIC16F689	PIC18F2220	PIC12C509	PIC16C74B
PIC16F690	PIC18F2221	PIC12C509A	PIC16C76
PIC16F716	PIC18F2320	PIC12C671	PIC16C765
PIC16F72	PIC18F2321	PIC12C672	PIC16C77
PIC16F73	PIC18F2331	PIC12CE518	PIC16C770
PIC16F737	PIC18F2410	PIC12CE519	PIC16C771
PIC16F74	PIC18F2420	PIC12CE673	PIC16C773
PIC16F747	PIC18F2423	PIC12CF674	PIC16C774
PIC16F76	PIC18F2431		PIC16C781
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PTC16F785	PTC18F2510	PTC16C54C	PTC16C925
PTC16F818	PTC18F2515	PTC16C55	PTC16C926
PTC16F819	PTC18F2520	PTC16C554	PTC16CF623
PTC16F83	PTC18F2523	PTC16C558	PTC16CF624
PTC16F84	PTC18F2525	PTC16C554	PTC16CE624
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Notes